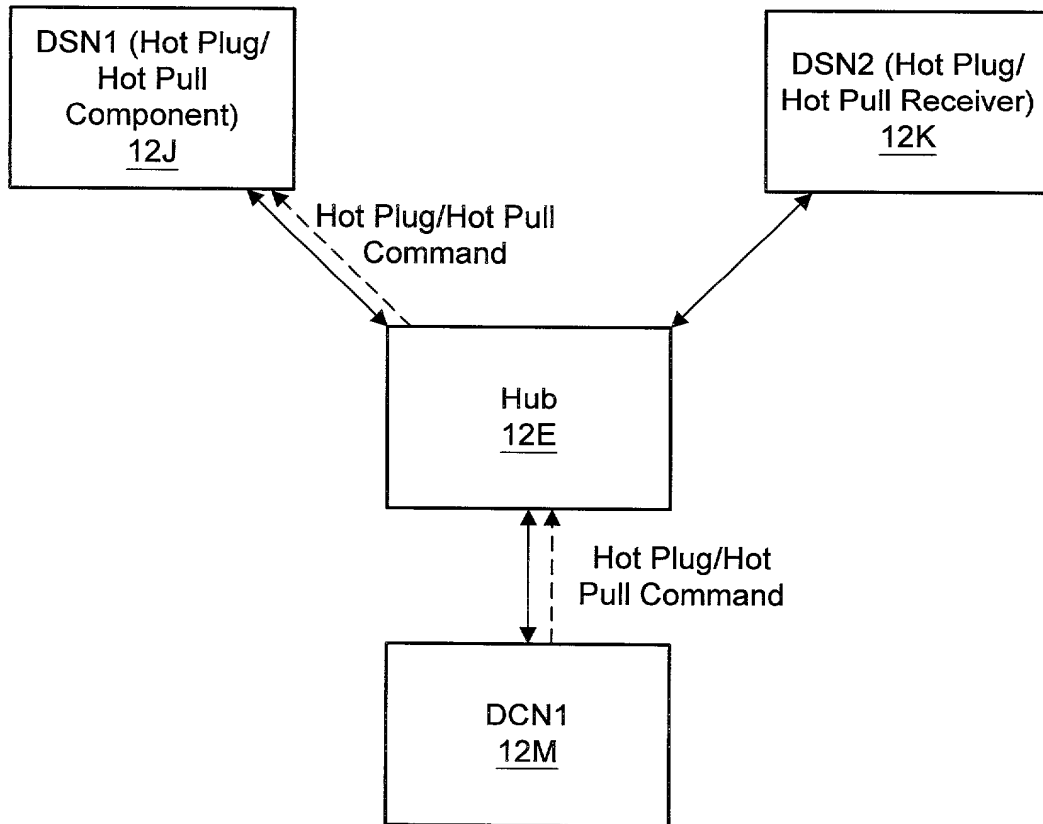


Fig. 1

10



*Fig. 2*

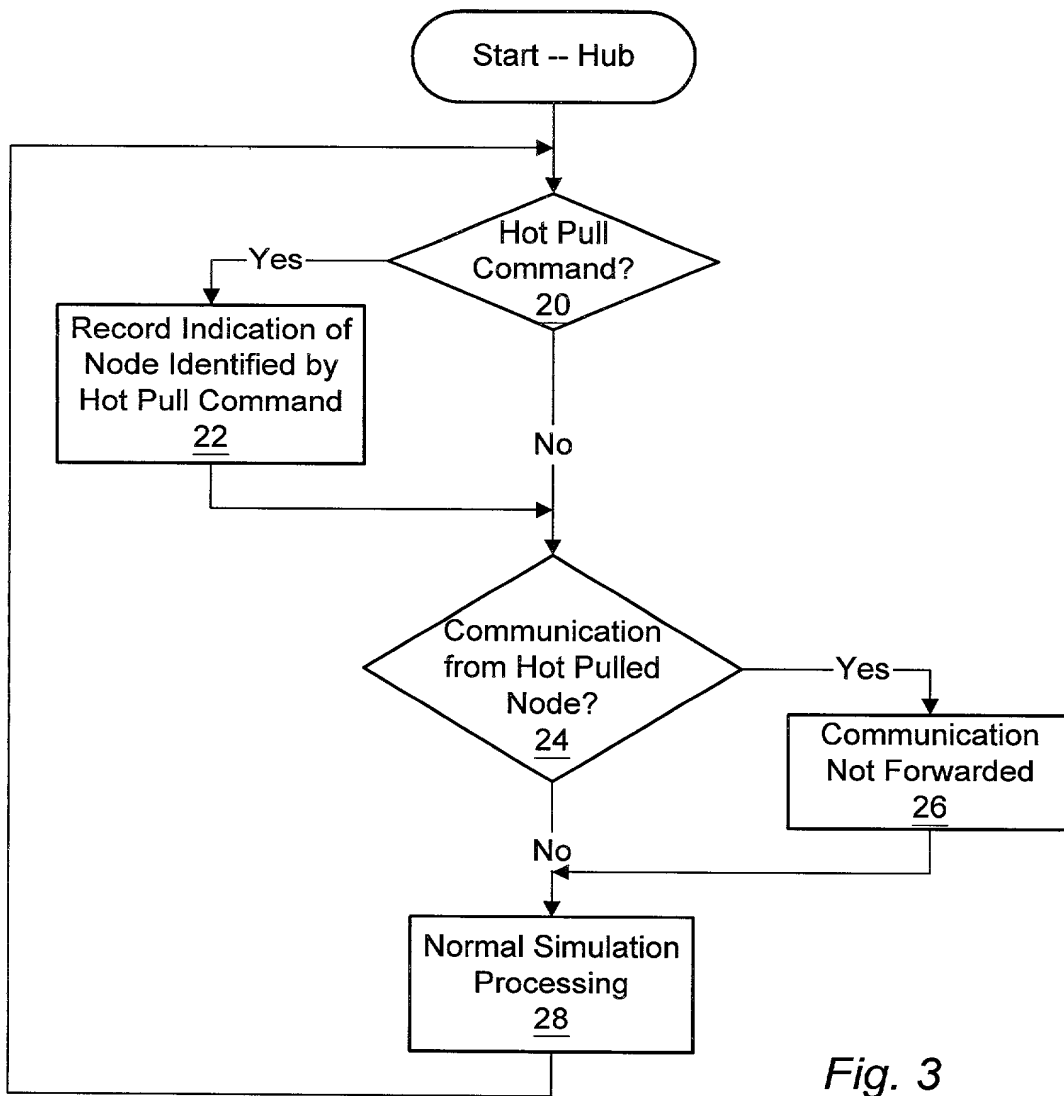
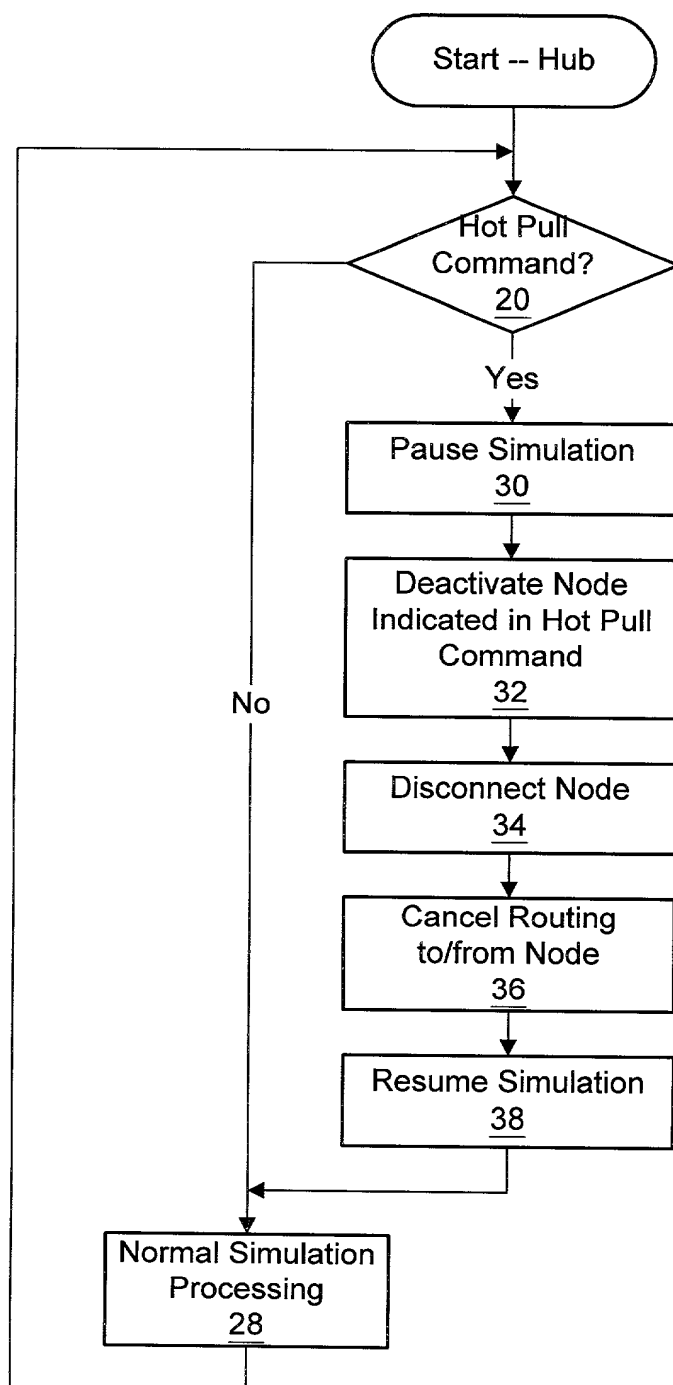
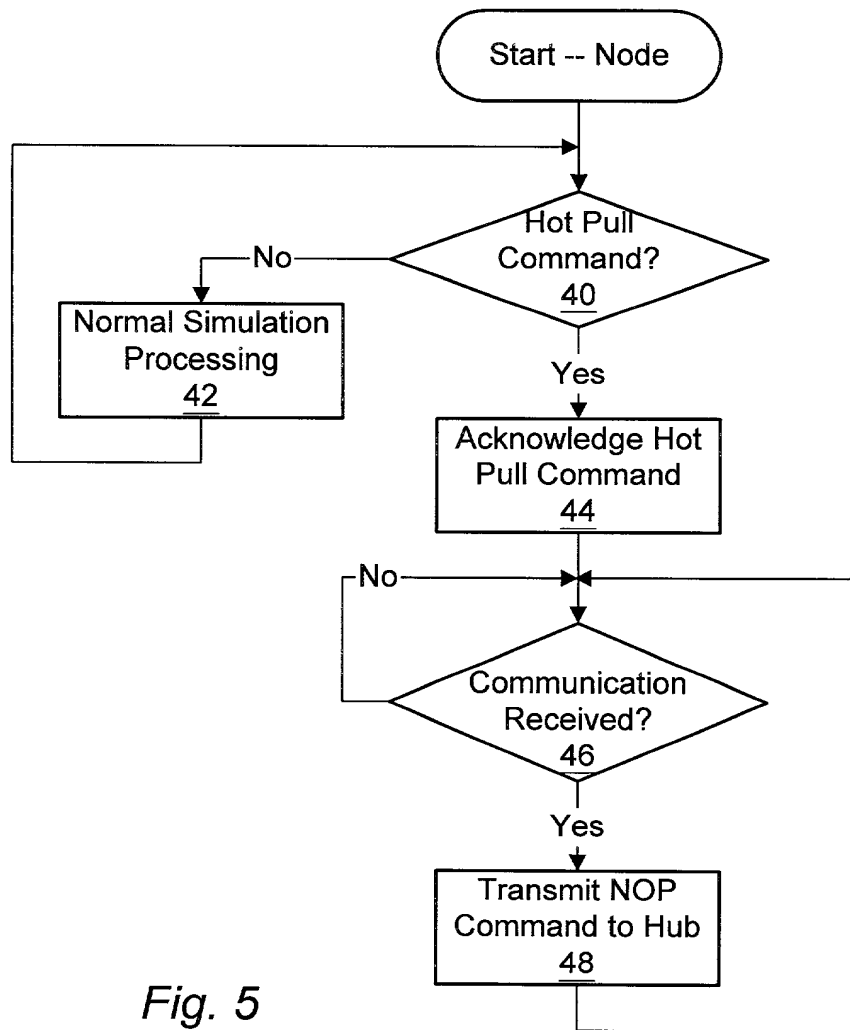
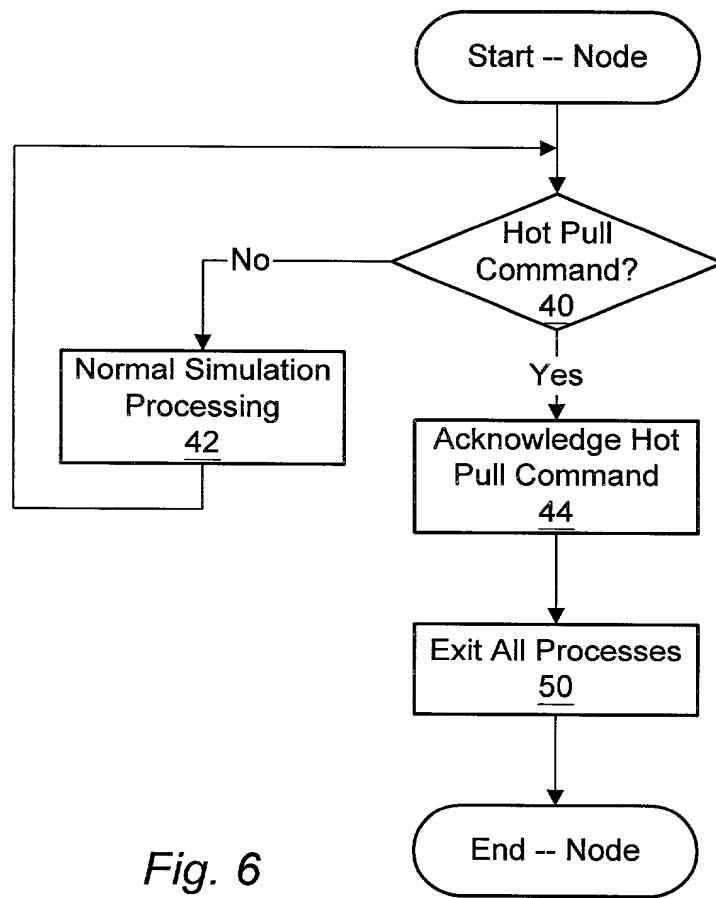
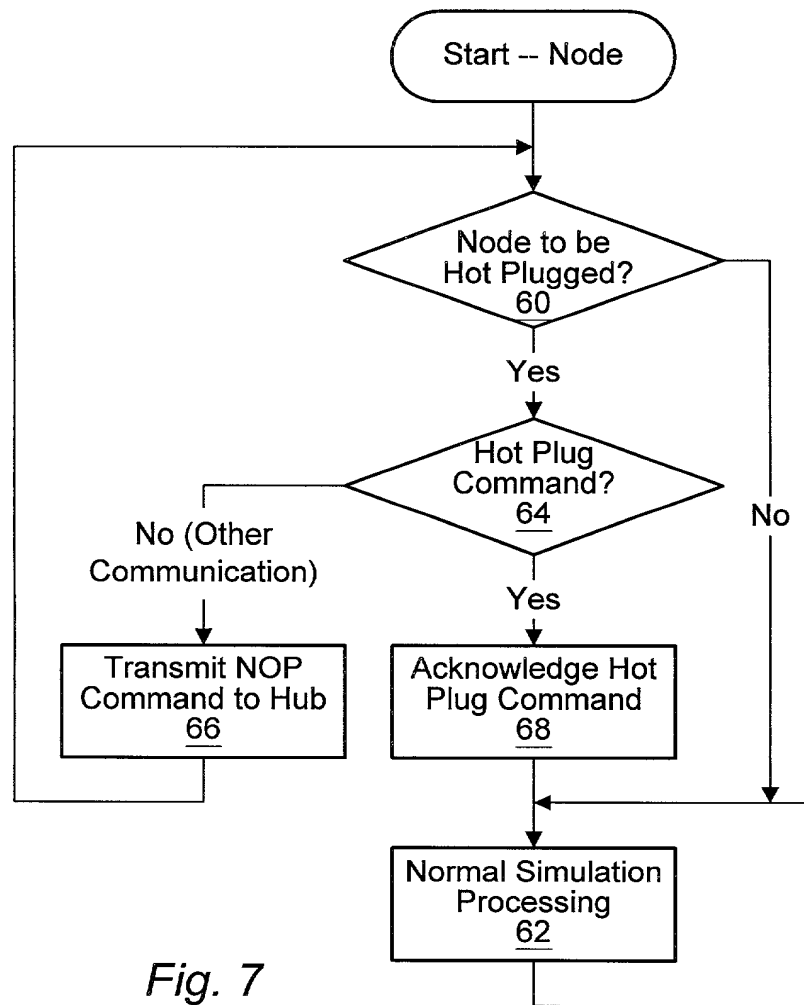


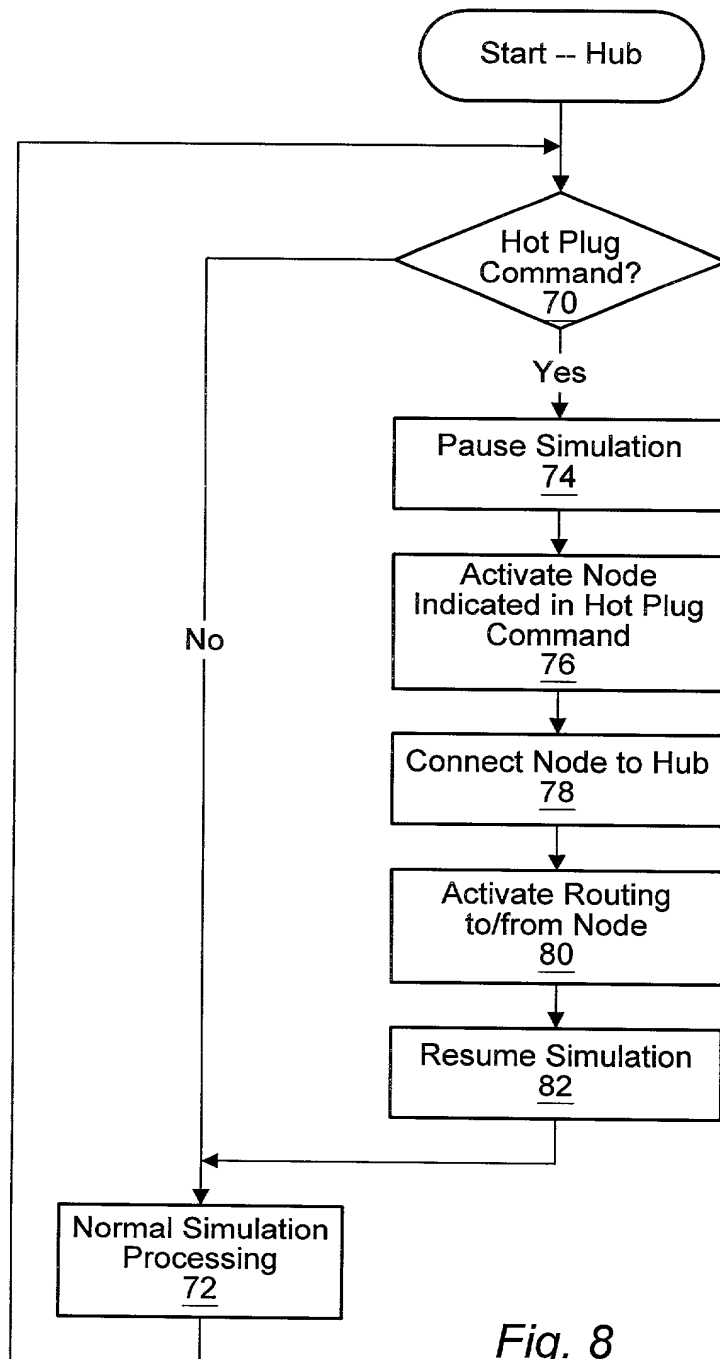
Fig. 3

*Fig. 4*

*Fig. 5*

*Fig. 6*

*Fig. 7*

*Fig. 8*



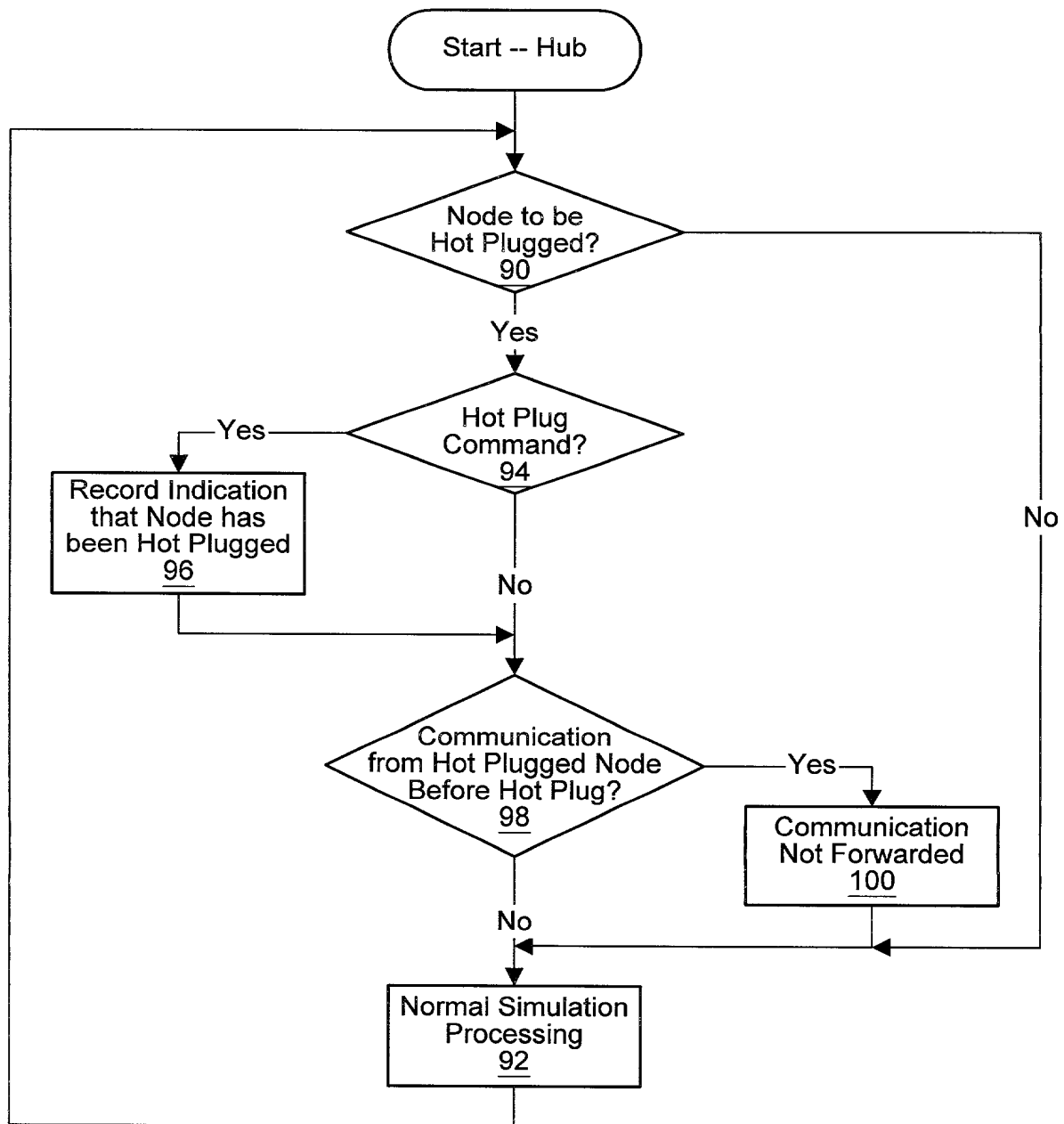


Fig. 9

```

scf : SCF '{' scf_description '}'
;
scf_description : | model_instances routing
;
model_instances : one_instance more_instances
;
one_instance : BASENAME NAME ','
;
more_instances : | more_instances one_instance
;
routing : | routing routing_exp
;
routing_exp : SCOPENAME1 '->' SCOPENAME1 ','
;

```

*Fig. 10*

```

SCF {
    chip1 dsn1;
    chip2 dsn2;
    dcn dcn1;

    dsn1.all_ports -> dsn2.all_ports;
    dsn2.all_ports -> dsn1.all_ports;
    dcn1.all_ports -> dsn1.all_ports;
}

```

*Fig. 11*

```

SCF {
    dsn1.all_ports -> dsn2.all_ports;
    dsn2.all_ports -> dsn1.all_ports;
}

```

*Fig. 12*

```

scf : SCF '{' scf_description '}'
      ;
scf_description : | model_instances routing
      ;
model_instances : one_instance more_instances
      ;
one_instance : BASENAME NAME ';'
              | HOTPULL BASENAME NAME ';'
              | HOTPLUG BASENAME NAME ';'
      ;
more_instances : | more_instances one_instance
      ;
routing : | routing routing_exp
      ;
routing_exp : SCOPENAME1 '->' SCOPENAME1 ';'
      ;

```

*Fig. 13*

```

SCF {
    hotpull chip1 dsn1;
    chip2 dsn2;
    dcn dcn1;

    dsn1.all_ports -> dsn2.all_ports;
    dsn2.all_ports -> dsn1.all_ports;
    dcn1.all_ports -> dsn1.all_ports;
}

```

*Fig. 14*

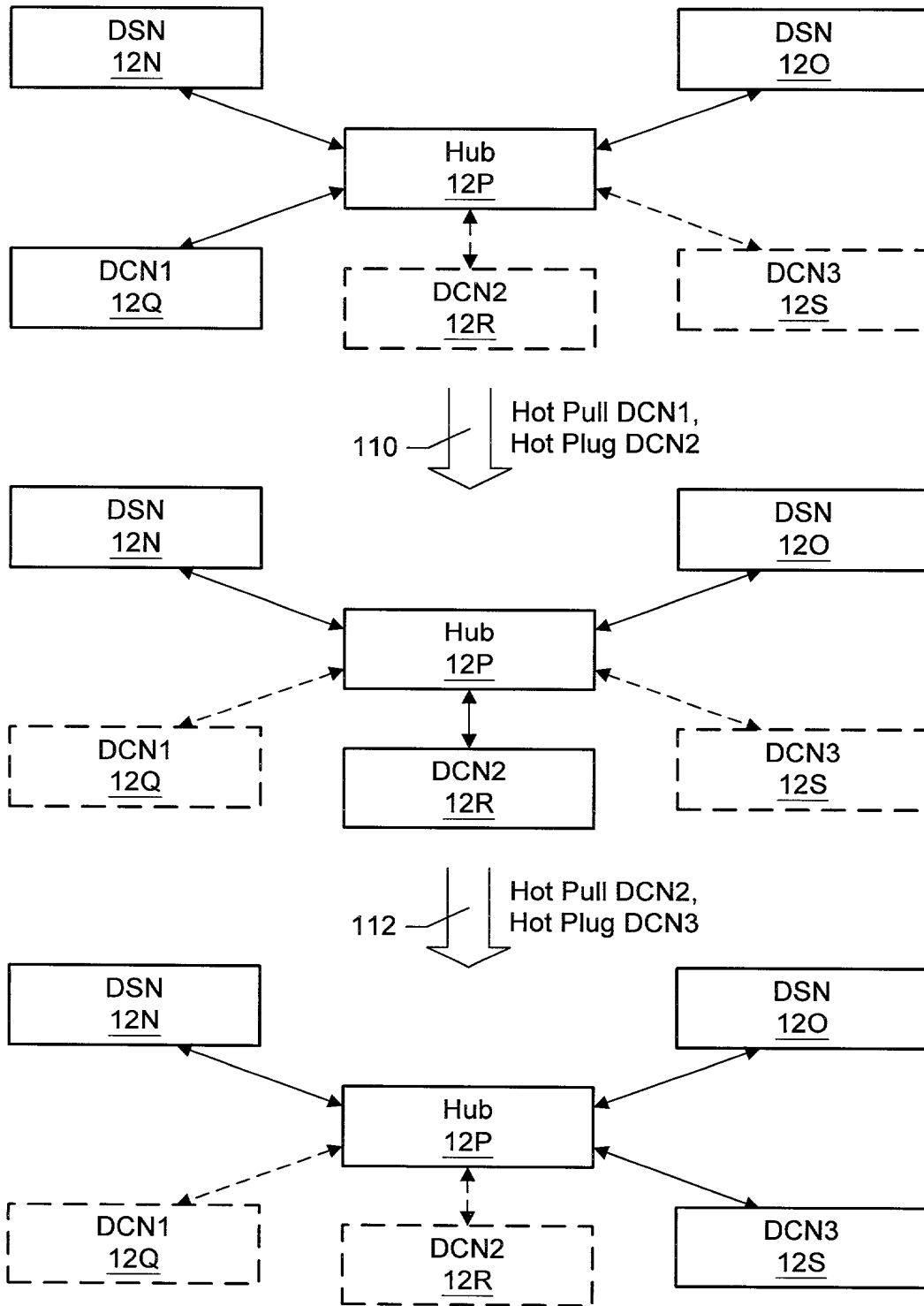


Fig. 15

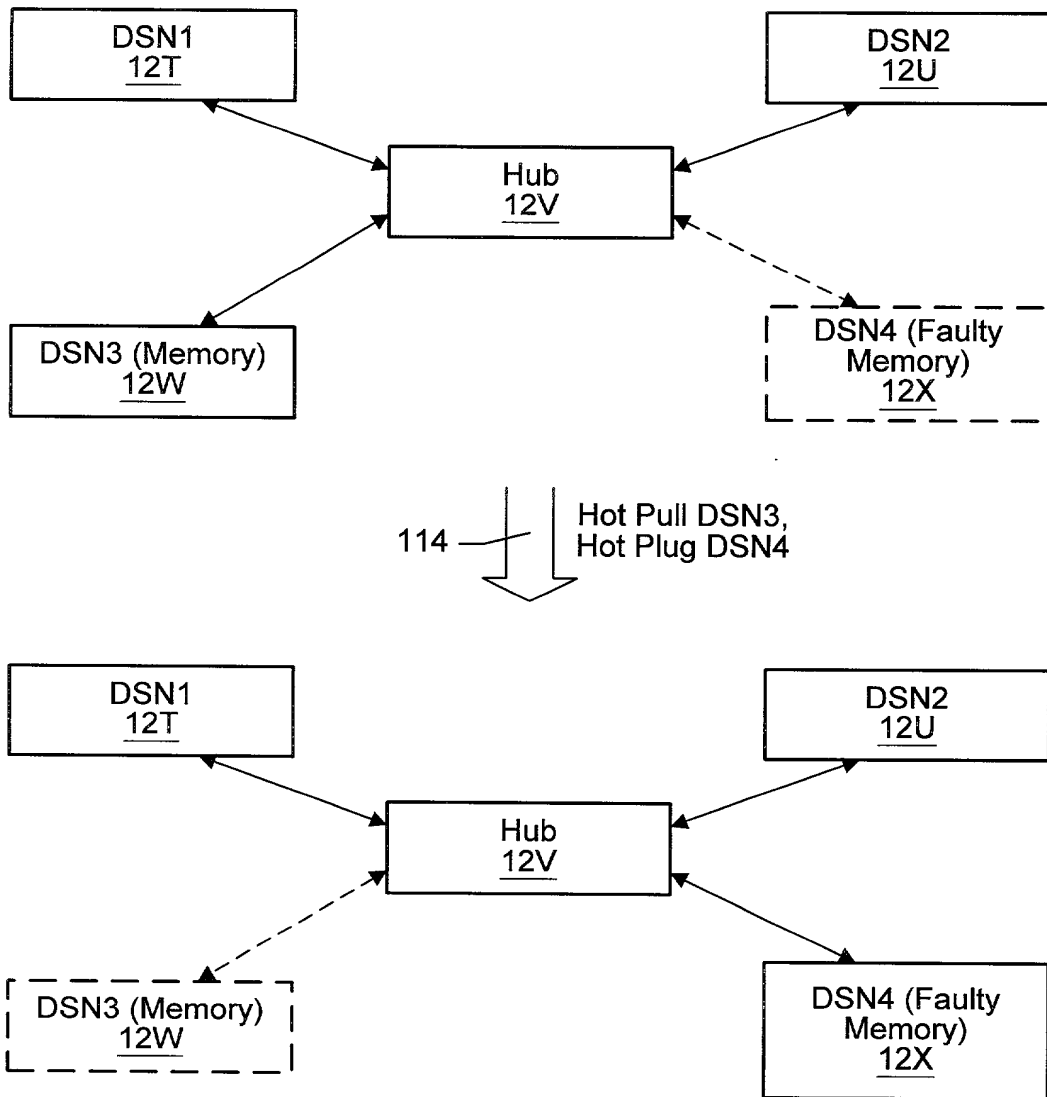


Fig. 16

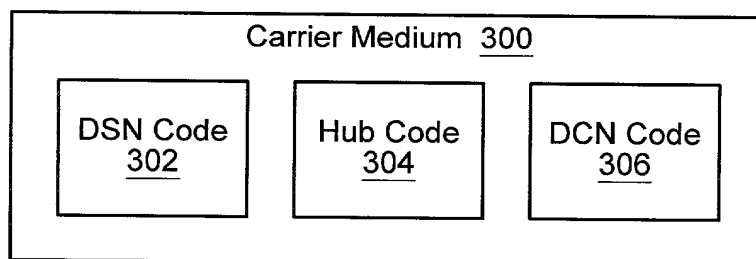


Fig. 17